



[02885/3A]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s) : Martin VORBACH et al.  
Serial No. : 09/290,342  
Filed : April 12, 1999  
For : DATA PROCESSING SYSTEM  
Examiner : Whitmore  
Art Unit : 2812

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MICHELLE M. CARNIAUX  
KENYON & KENYON

RESPONSE

Sir:

This paper addresses the Office Action mailed on  
January 16, 2003.

REMARKS

Claims 69-79 and 81 have been withdrawn from  
consideration. Applicants gratefully acknowledge the  
Examiner's indication that claims 21-28, 32-34, 37, 49, 59,  
63, 65-67, and 80 contain allowable subject matter.  
Reconsideration of the present application is requested.

I. REJECTION OF CLAIMS 57, 60 AND 61 UNDER 35 U.S.C. 102(b)

Claims 57, 60 and 61 stand rejected under 35 U.S.C.  
102(b), the Examiner contending these claims are anticipated  
by Saleeba, "A self-contained dynamically reconfigurable  
processor", 2/1993, 16<sup>th</sup> Australian Computer Science  
Conference (hereinafter "Saleeba").

Applicant's claim 57 is directed to a data processor  
having "cells arranged in a multi-dimensional pattern" and a  
compiler for "individually accessing and individually  
configuring" at least some of the cells.

To anticipate a claim, the reference must teach every

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